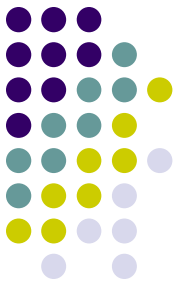


# Reading material



<http://www.cyberkineticsinc.com/>

<http://www.med64.com/>

<http://www.multichannelsystems.com/>

<http://www.qwane.com/>

# Moc/Bio and Nano/Micro

## Lee and Stowell

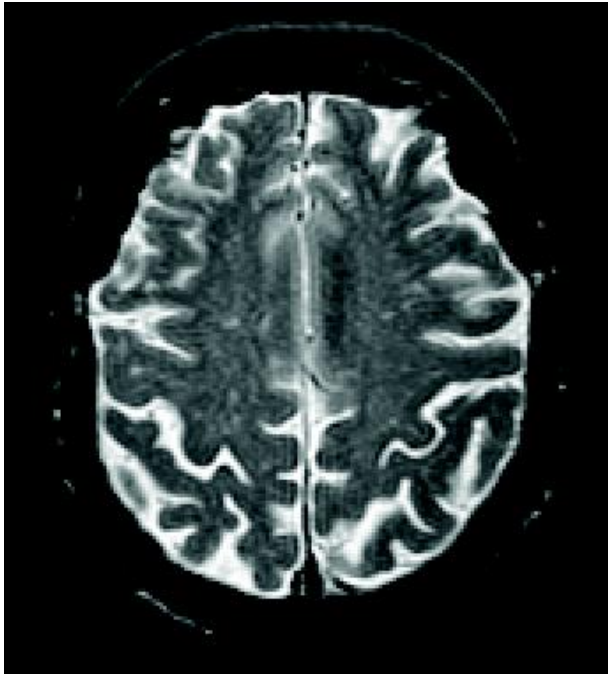
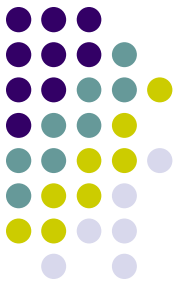
Moc/Bio-Lecture

Machine-Brain Interfaces

Brain  
Neurons  
Synapses  
Electrical Activity  
Electrical Interfaces  
LNN  
Implants  
Examples



# From Neural Systems to Neuromorphic MEMS



$10^{11}$  neurons

Each neuron has  $10^3$ - $5$  synaptic connections

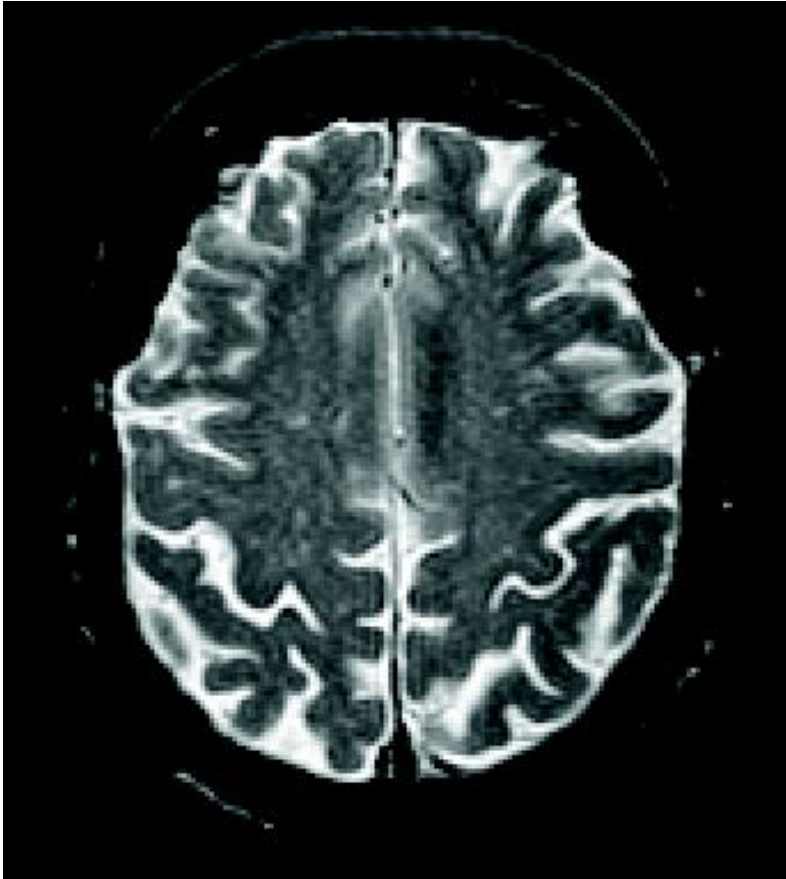
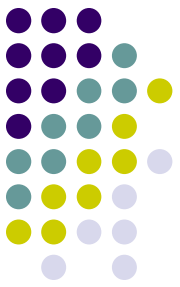
$\sim 10^{14}$  circuits/transistors

Pentium Processor =  $3 \times 10^6$

During development we make  $10^3$  connections/sec

As a child you build a Pentium Processor in your head every hour!

The human brain contains more transistors than all the computing power produced on earth up to 2001!

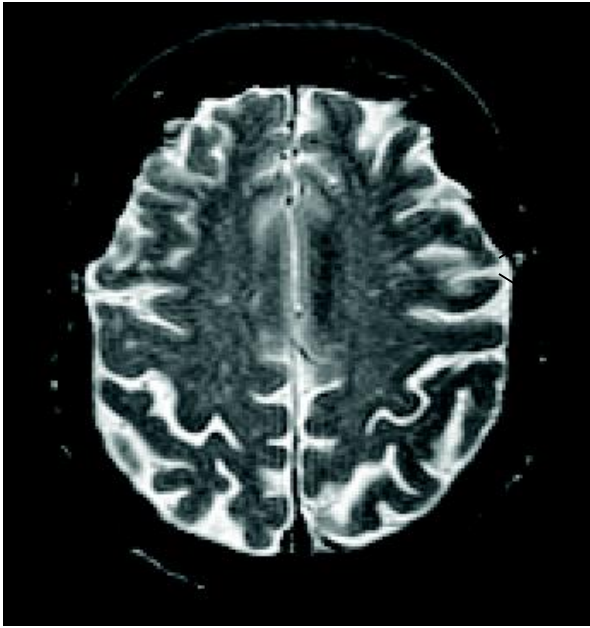


Highly parallel

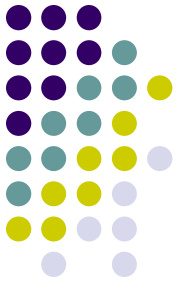
Self repairing

Plasticity

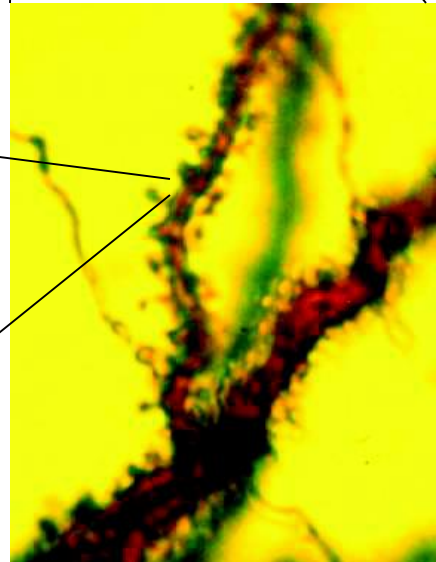
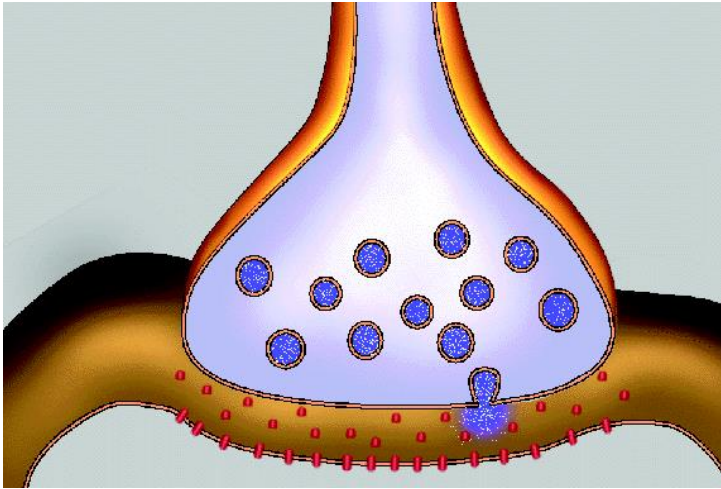
Quantum?



$10^9$

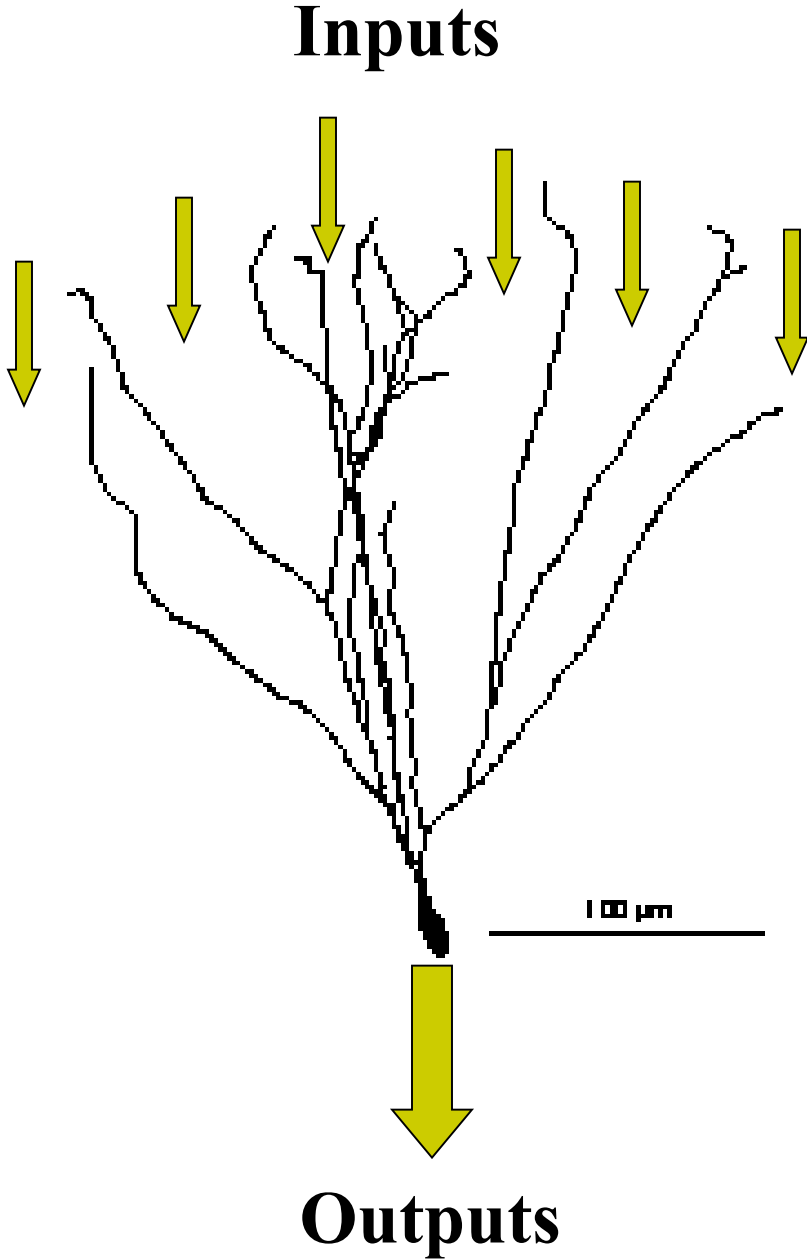
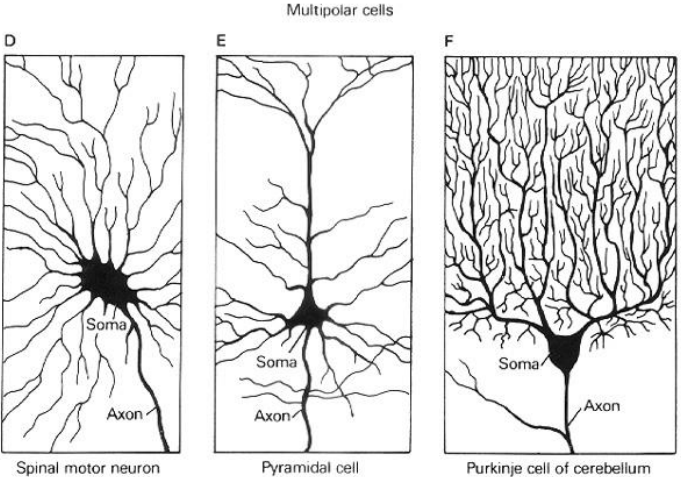
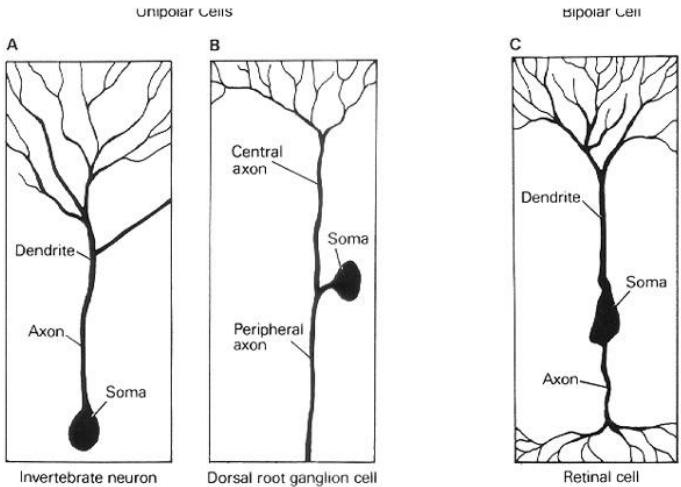
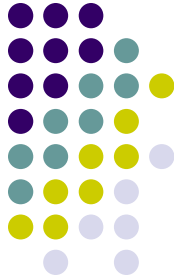


$10^3$



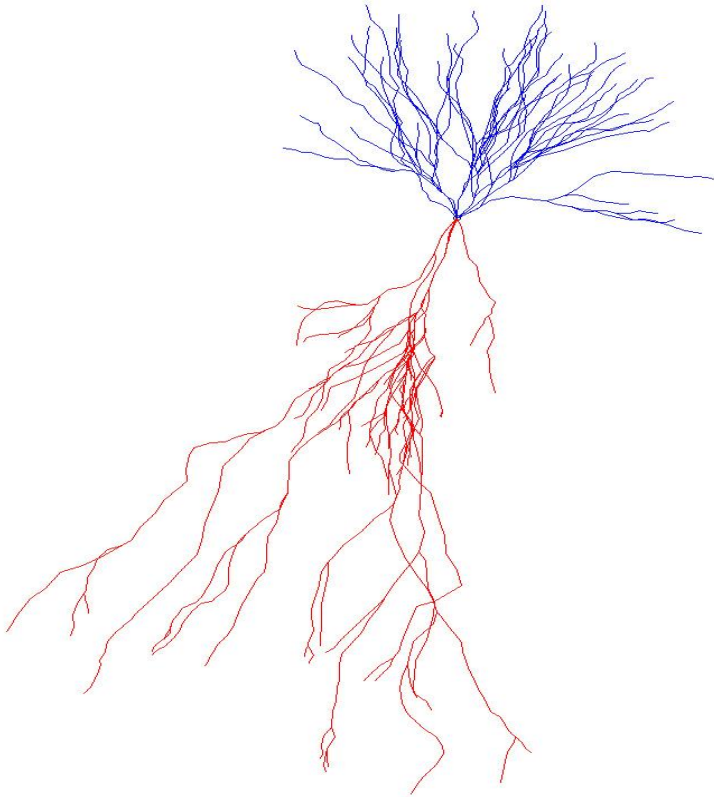
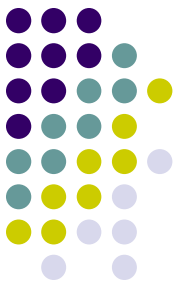
$10^2$

# Neurons

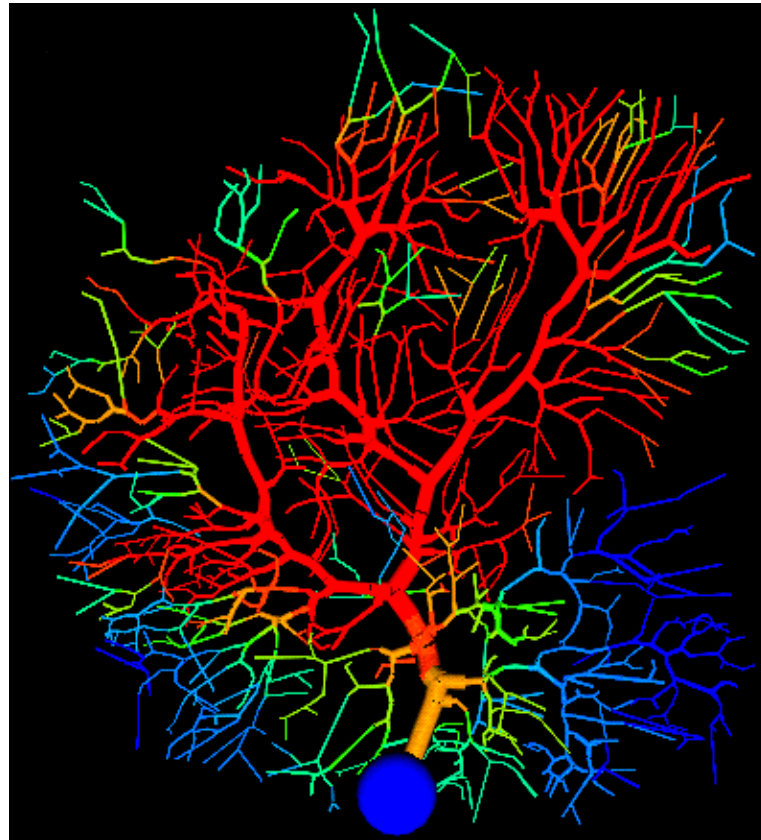




# Numbers of Inputs (synapses)

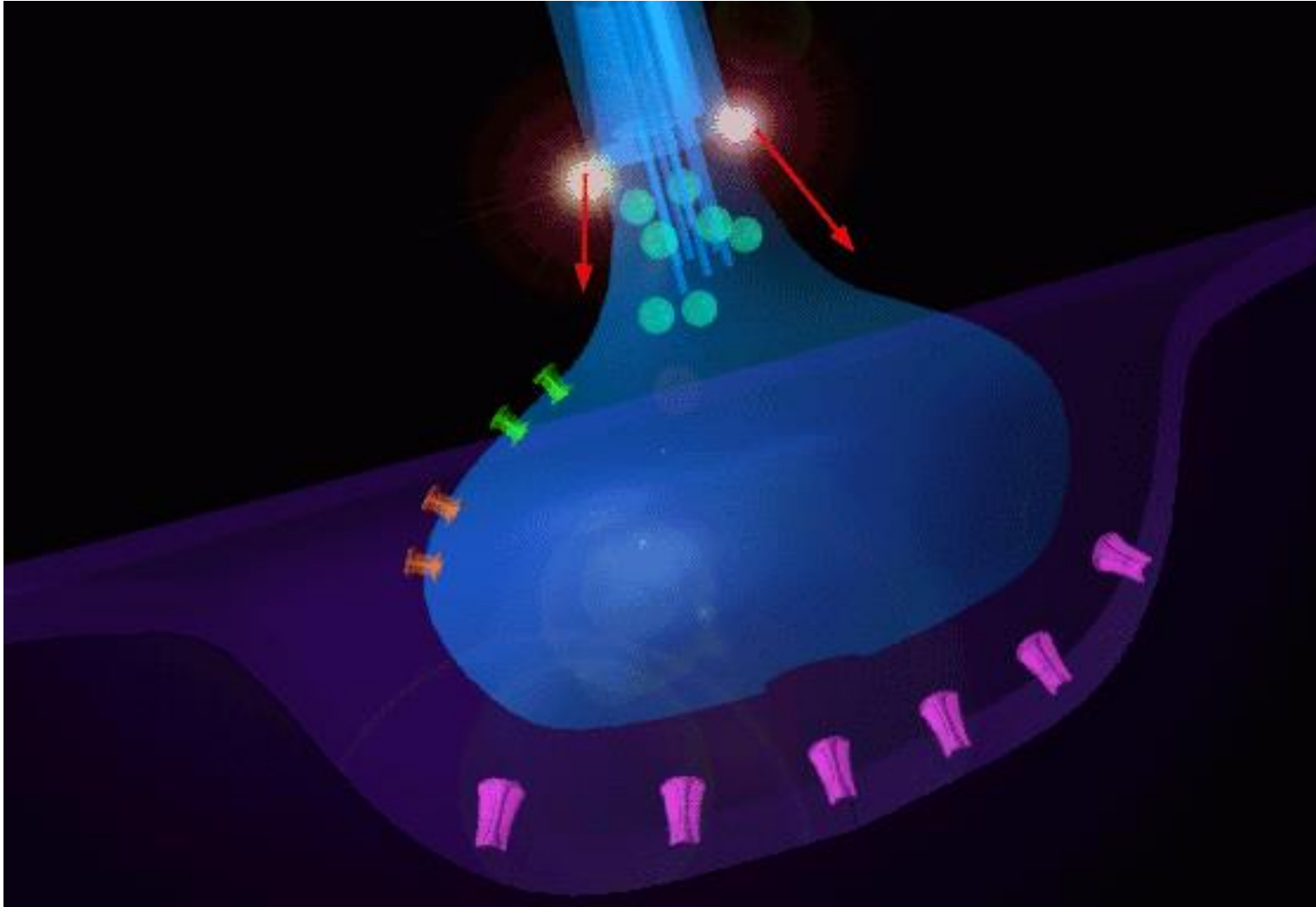
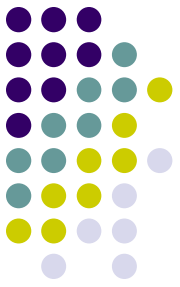


Pyramidal ~1000



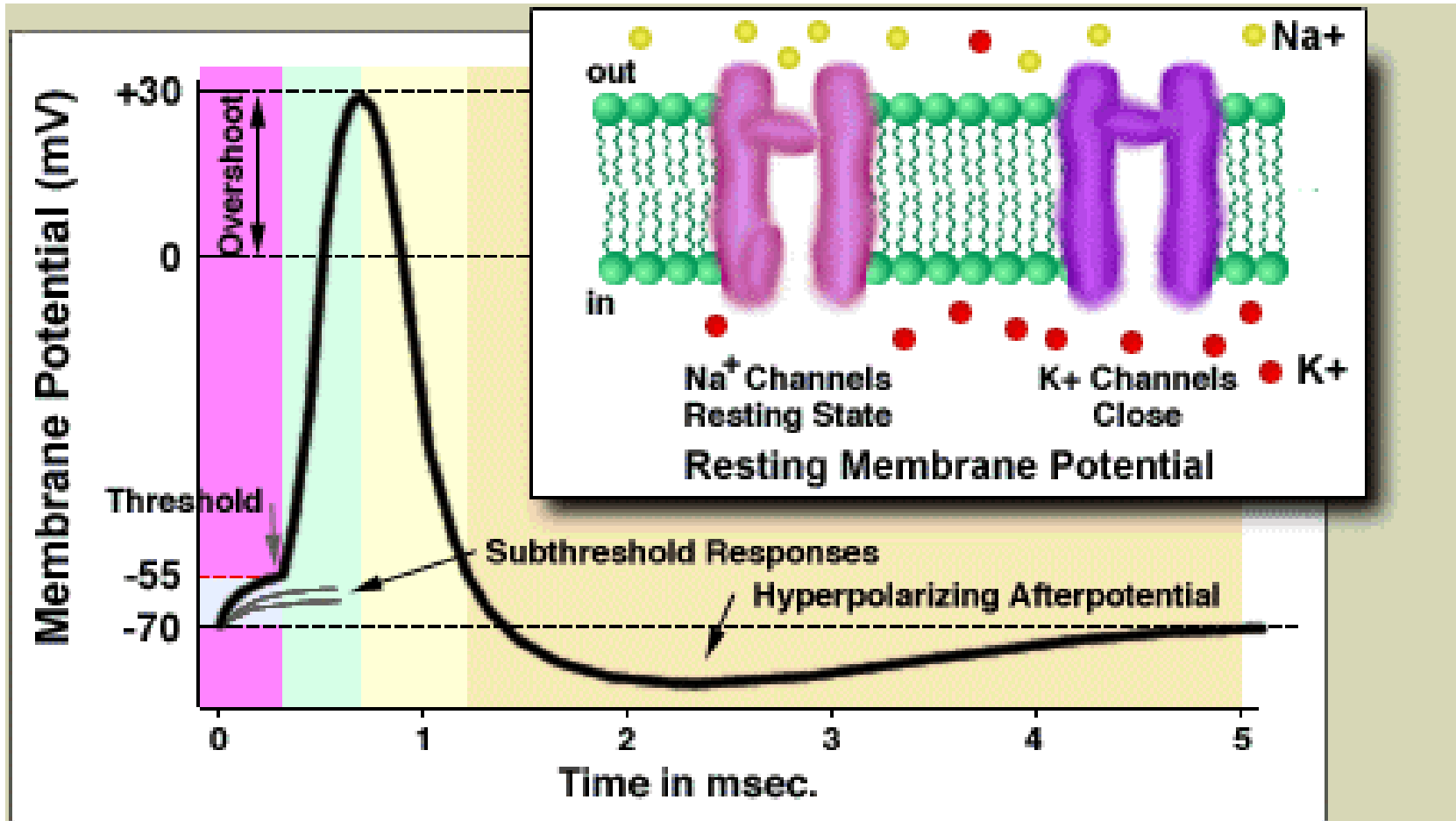
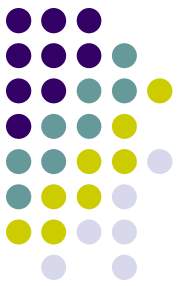
Purkinje >100,000

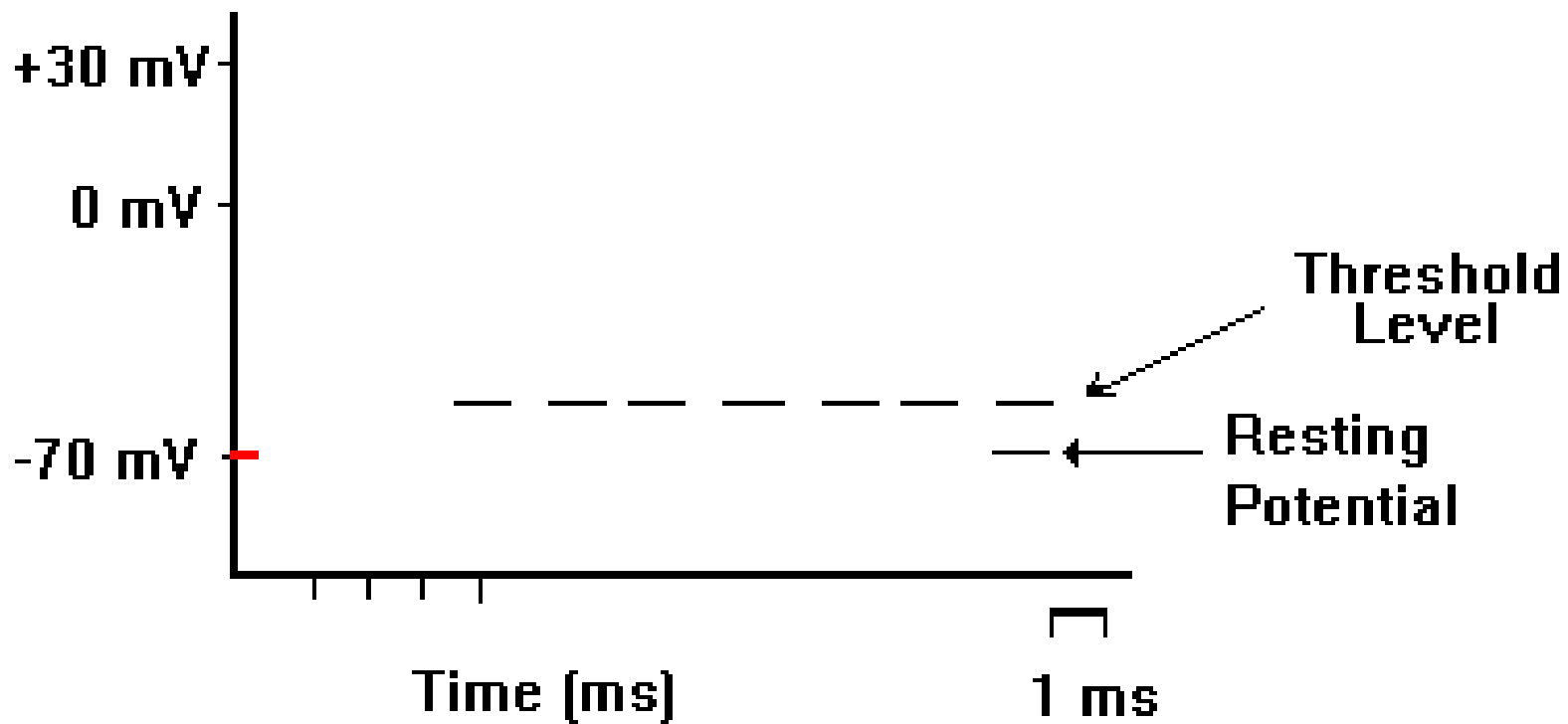
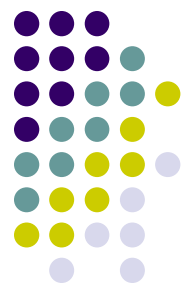
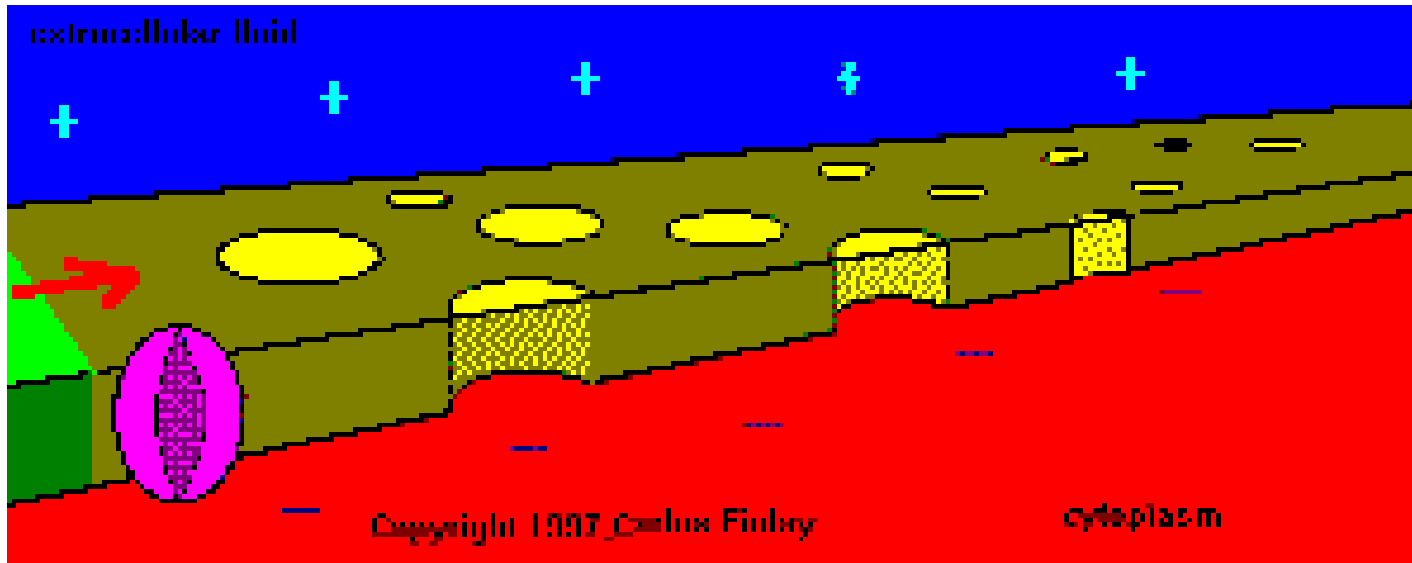
# Synapse (connections/transistors)



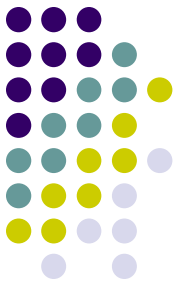


# Electrical activity



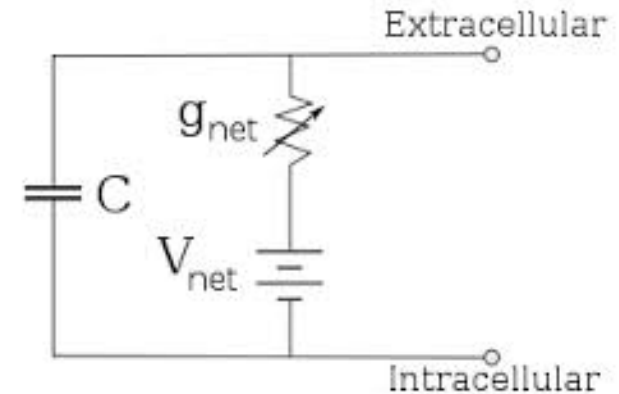


# How about some physical numbers

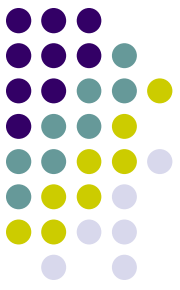


**Neuron:** 50 microns in diameter  
**Dendrites:** 10 – 0.1 microns in diameter  
**Synapse:** < 1 micron squared

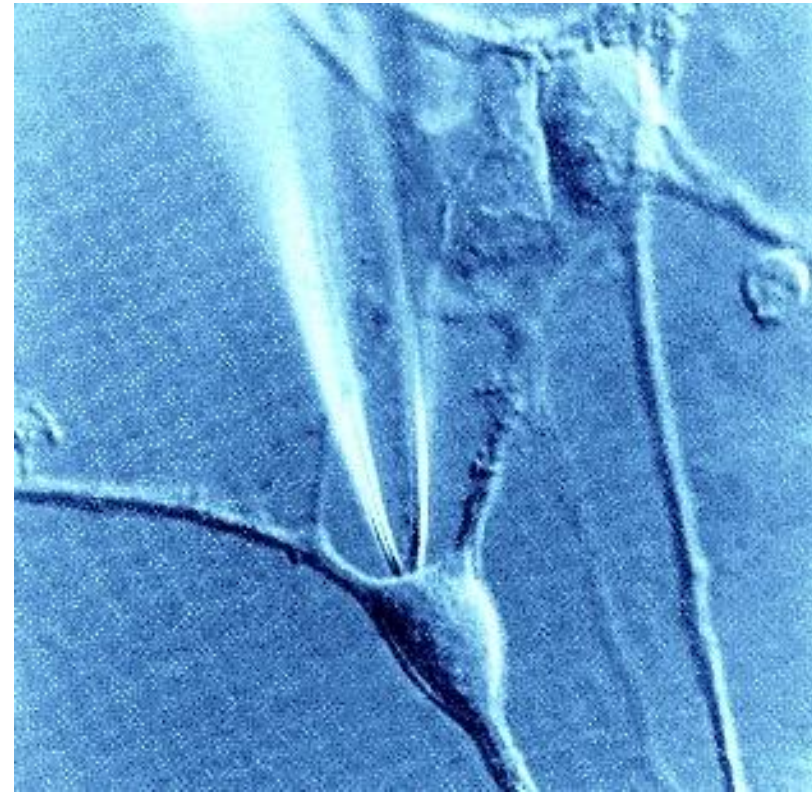
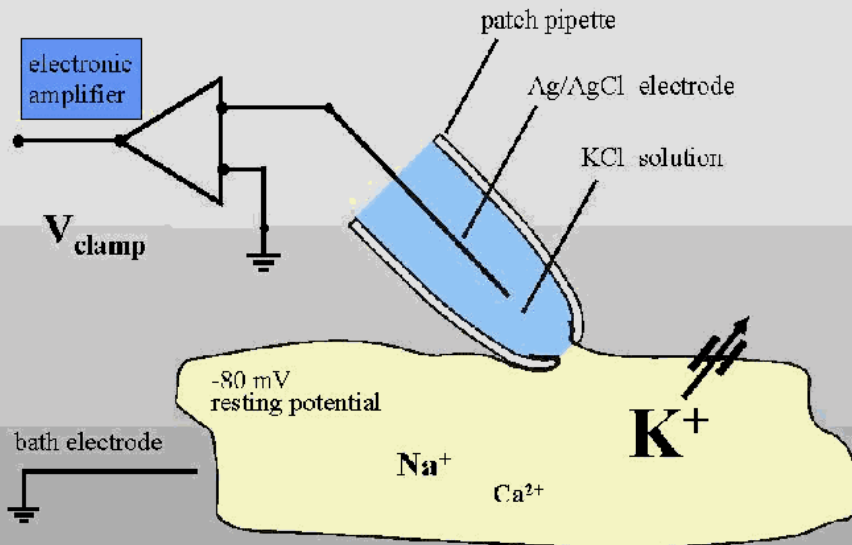
**Capacitance:**  $1\mu\text{F}/\text{cm}^2$   
**Potential:**  $-98\text{mV}$  ( $\text{K}^+$  out/in)  
**Resistance:** 10 to  $10^6$  Ohms  $\text{cm}^2$   
**Current:**  $10^{-7}$  C/ $\text{cm}^2$  ( $10^{-12}$  mol/ $\text{cm}^2$ )

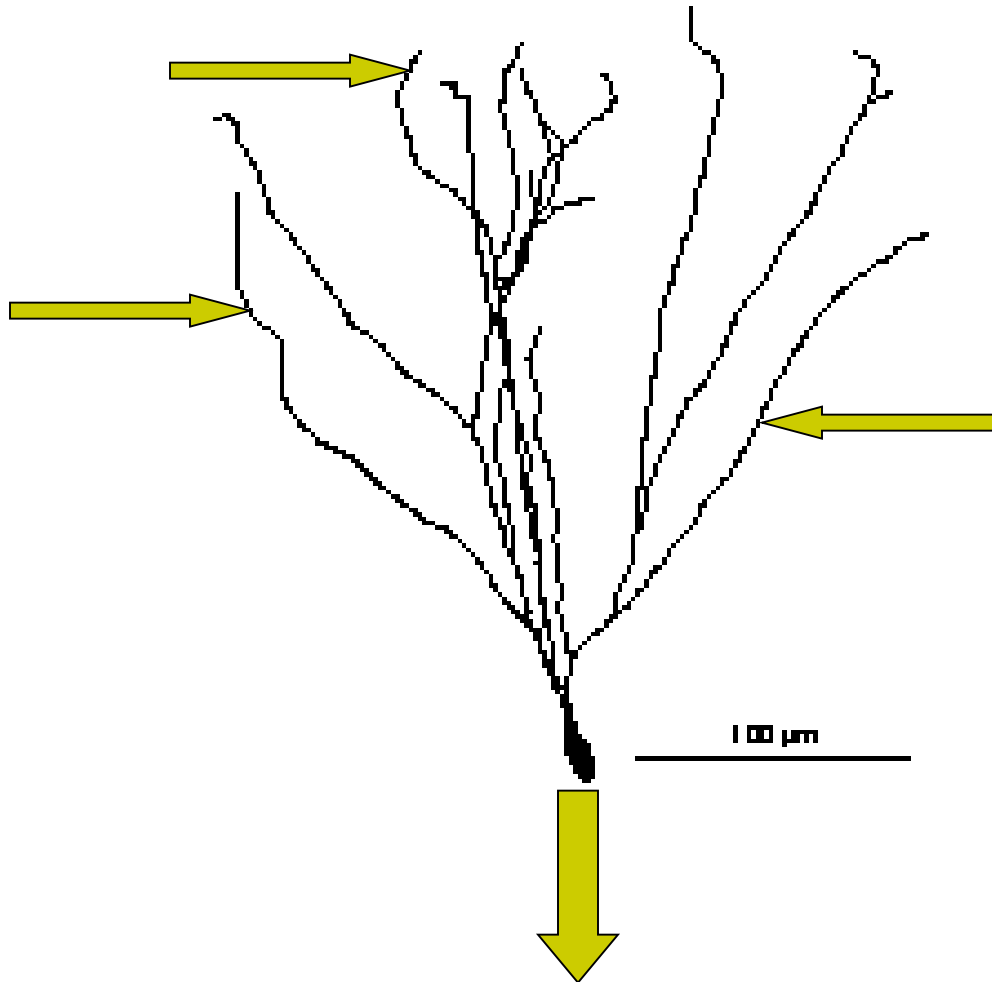


# Measurement and stimulation of neurons (patch clamp)



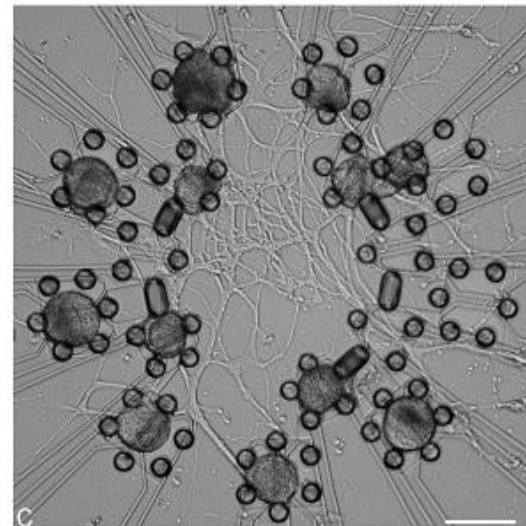
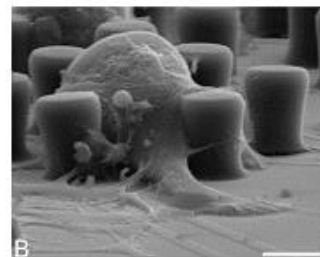
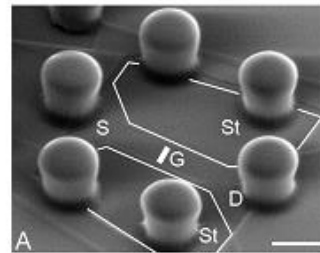
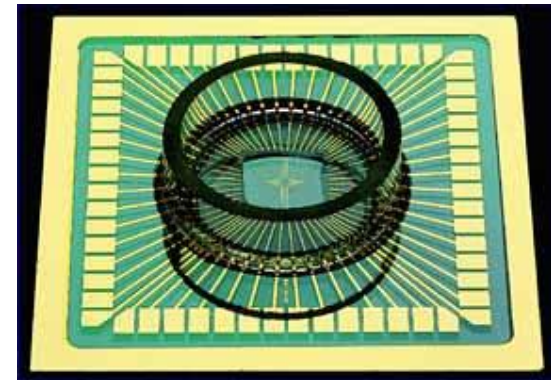
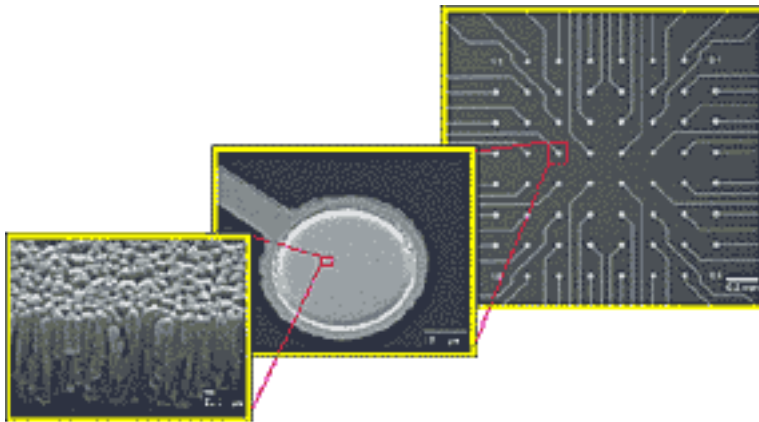
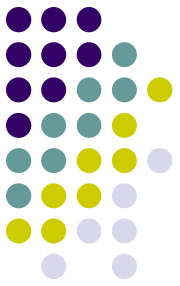
Whole cell *patch clamp* technique





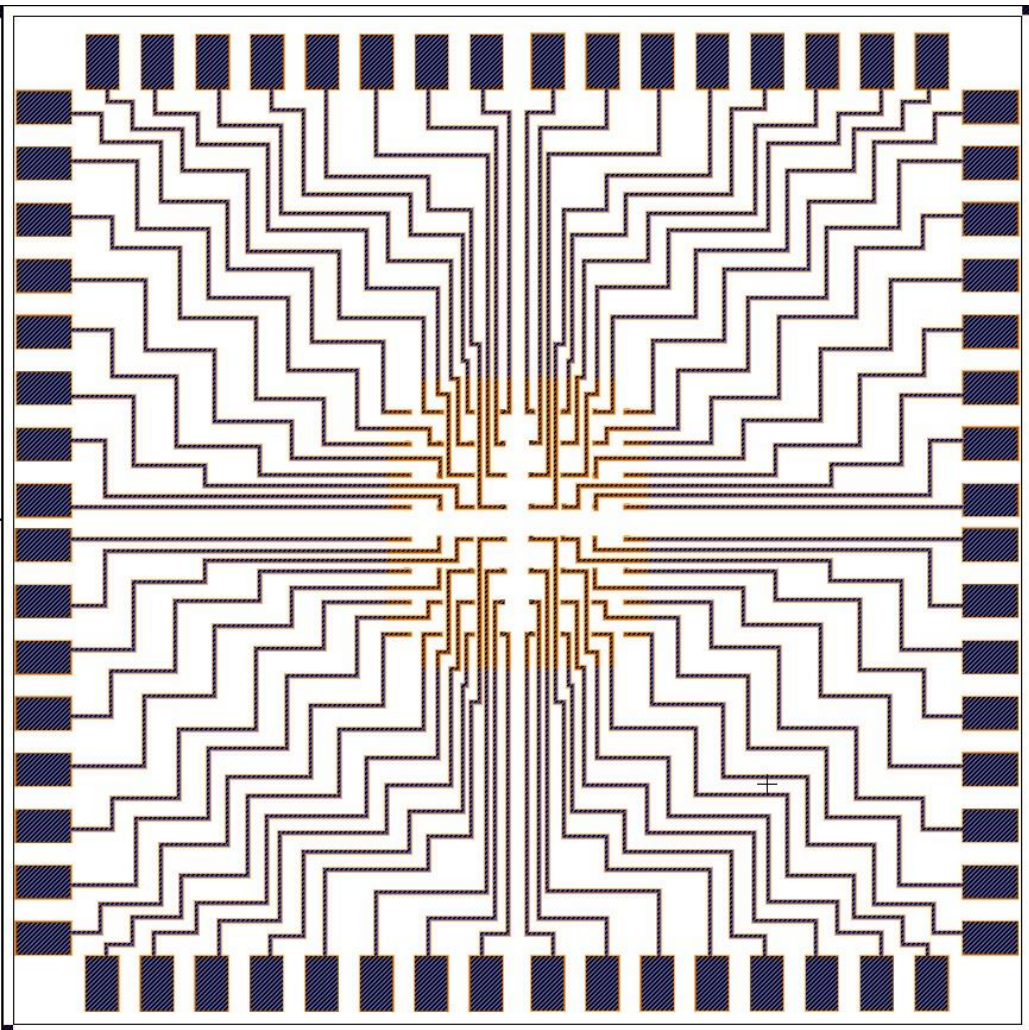
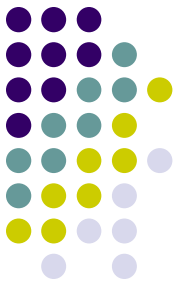
Few sites and invasive

# Multielectrode arrays



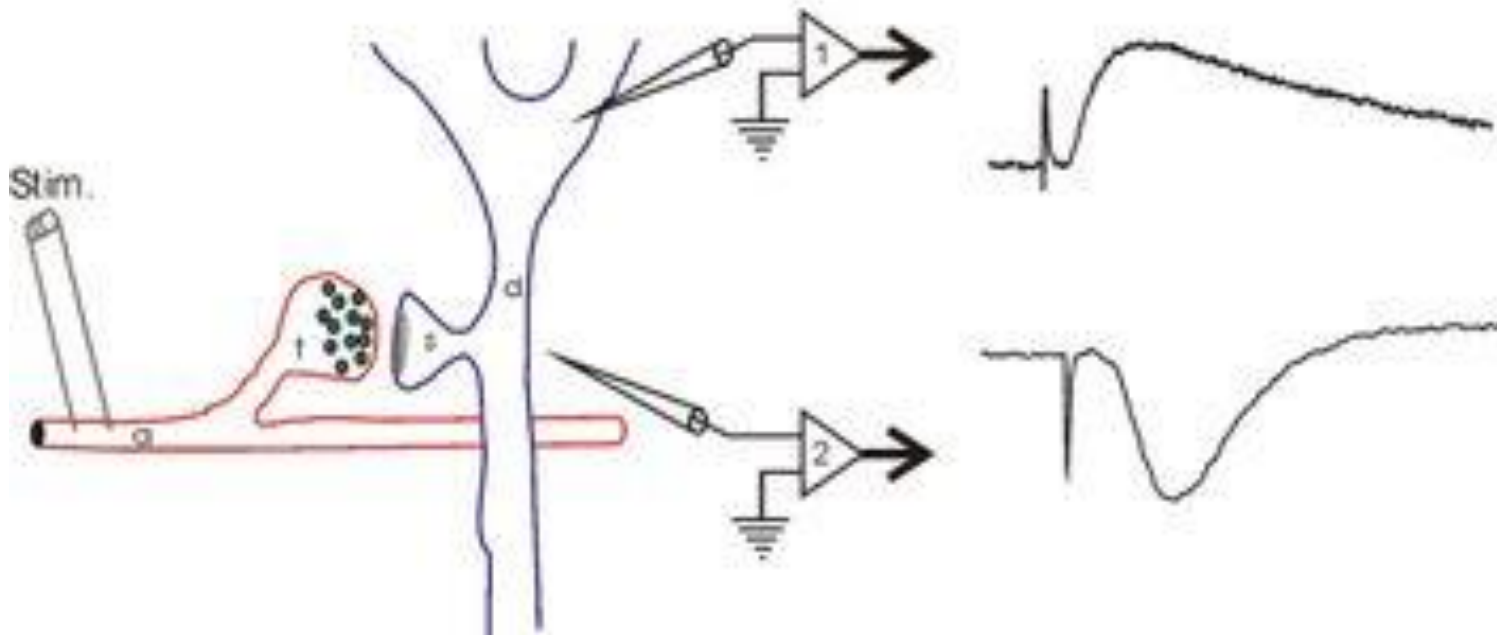
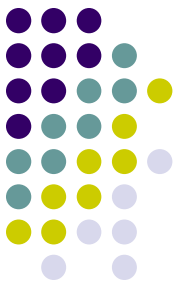


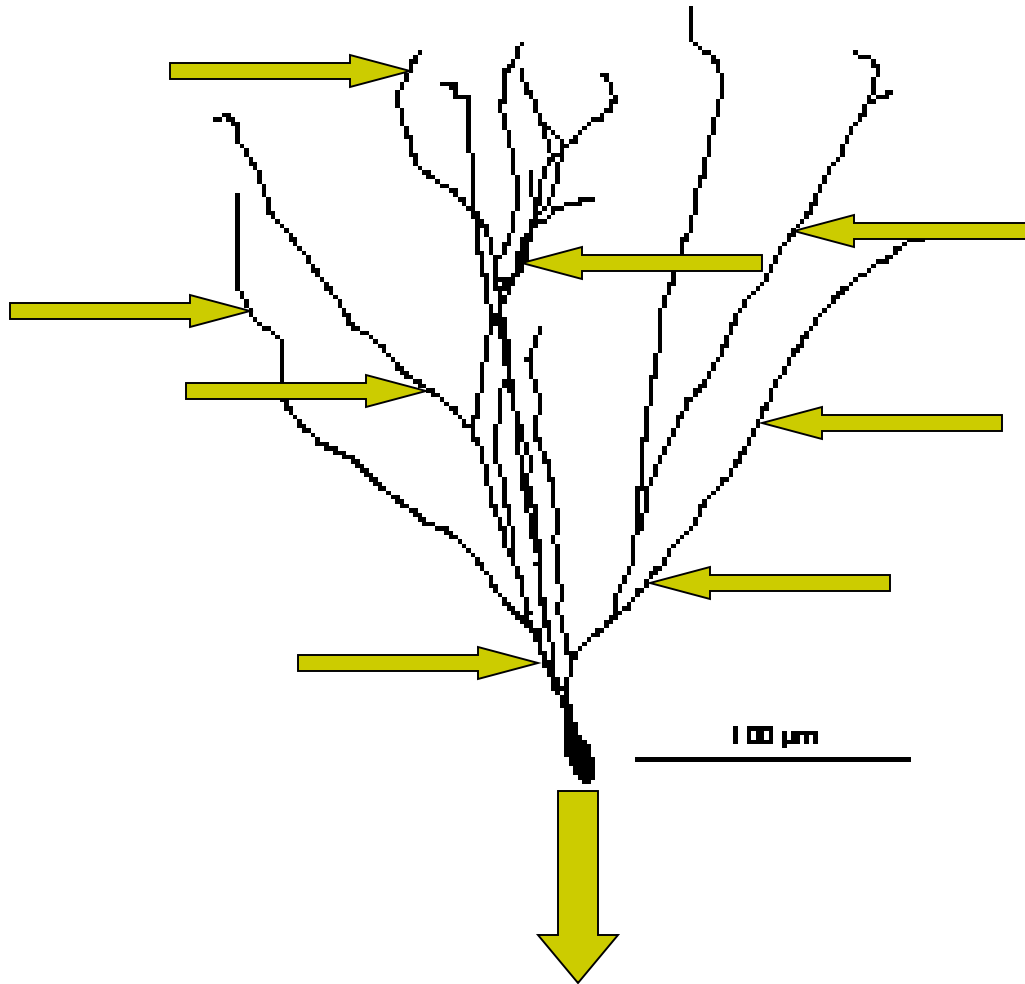
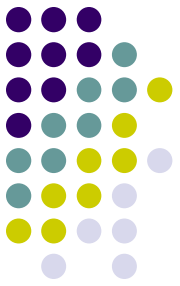
# Electrode Arrays with Small Pads



Pads: 5x5, 10x10, 20x20, 50x50

# How field effect electrodes record. Proximity is key.



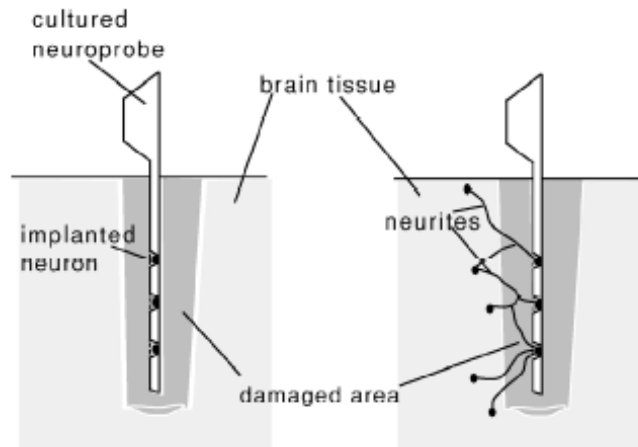
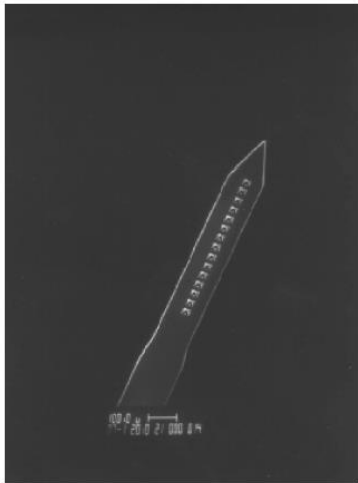
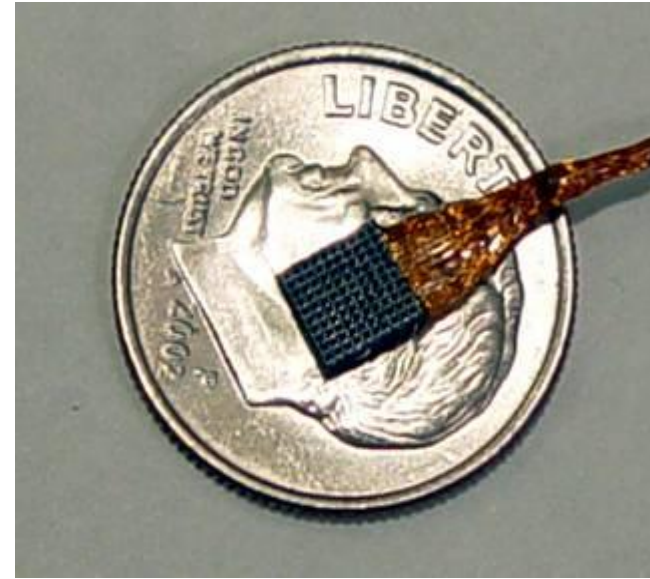
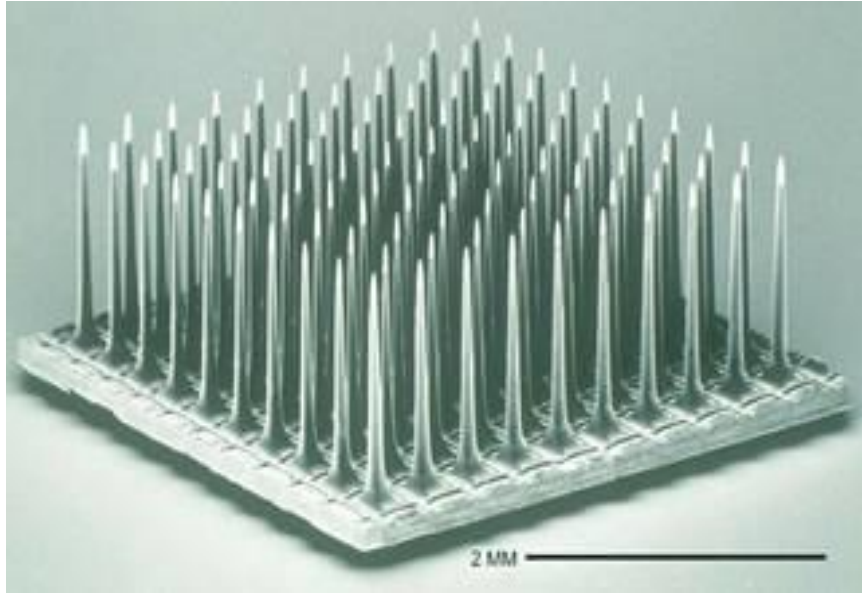
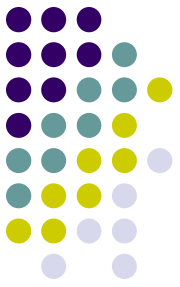


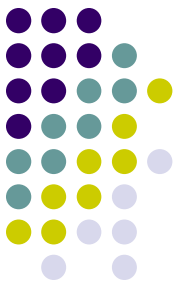


# How do we compute?

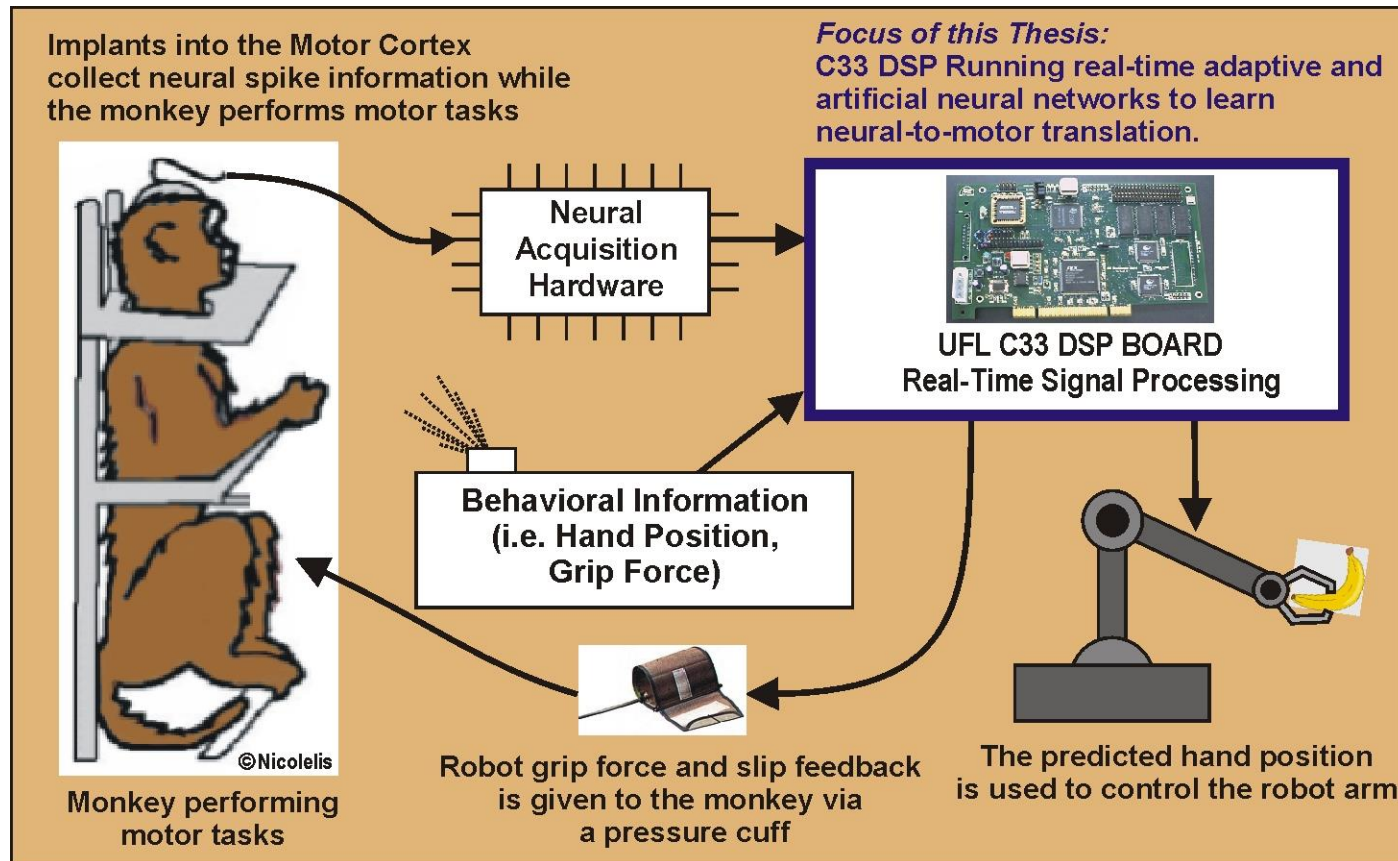
- Learning
  - what is the process
- Memory
  - what is the physical basis
- Repair
  - how does this occur and can we influence it
- Knowing this can we influence computation?
- Knowing this can we create wetware devices?

# Implantable devices (Johnny Mnemonic v0.1)

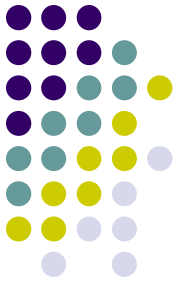




# Example motor movement







## Data Flow Diagram of Demonstration

Data stored in file from Monkey "Ivy"  
100ms Binned Spike Counts  
Data is streamed to DSP

External PCI  
10Hz Binned Spike Counts



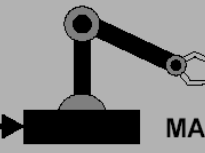
10Hz 3D Coordinates

TCP/IP over Ethernet  
10Hz (100ms) XYZ Coordinates  
Neural Reconstruction  
Coordinates

MATLAB Visualization showing  
actual vs. neural-reconstructed  
cursor coordinates



Robot Arm visualization  
showing the neural-reconstructed  
cursor coordinates



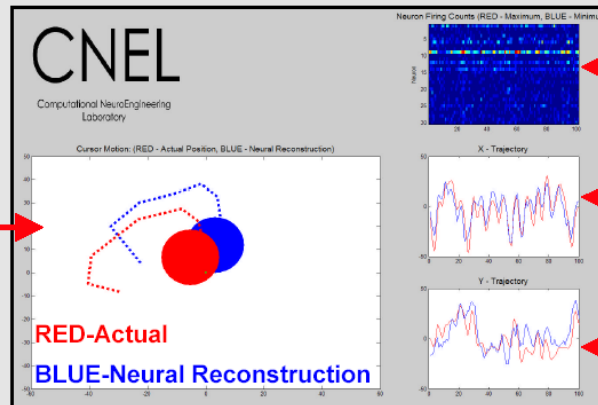
CAN Bus

MANUS Arm

## Visualization of Cursor Movement

Real-Time RNN in DSP  
Processing Neural Data from "Ivy"

Cursor Motion

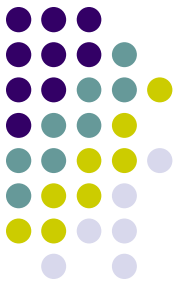


Neural Firing Counts  
from 30 neurons

X Trajectory

Y Trajectory

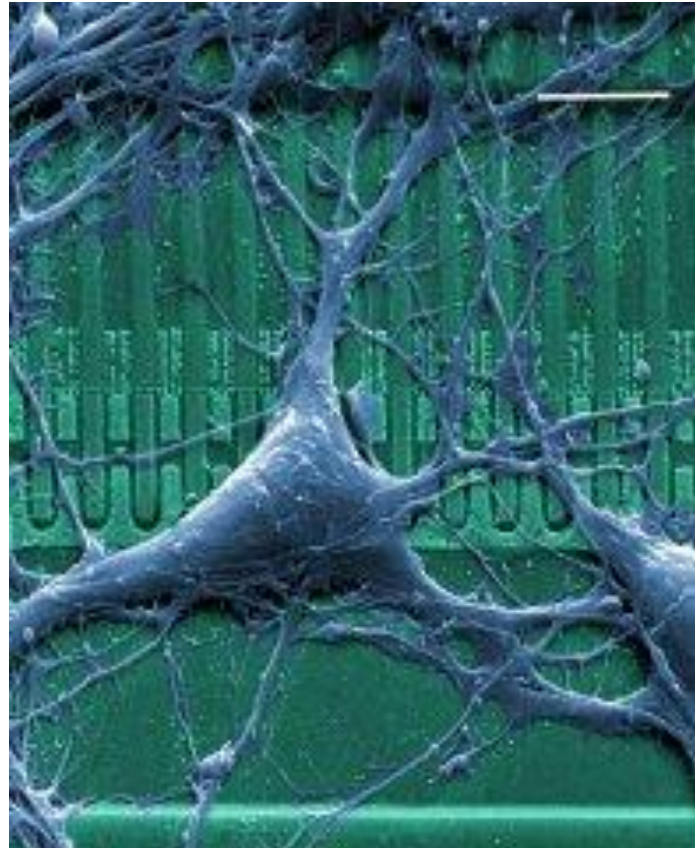
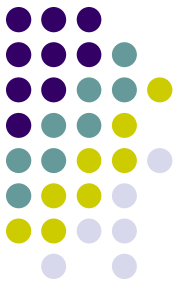
# Radio controlled rat



Pain and pleasure center electrode implants

<http://news.bbc.co.uk/2/hi/science/nature/1961798.stm>

# Emulating and integrating neural systems



Neural networks and fuzzy logic

Software

Floating-gate analog transistors

Wet computing devices (wetware)

Merging silicon with LNN

Neuromorphic MEMS

Hardware with wetware properties

# Emulating and integrating neural systems



Neural networks and fuzzy logic

Software

Floating-gate analog transistors

Wet computing devices (wetware)

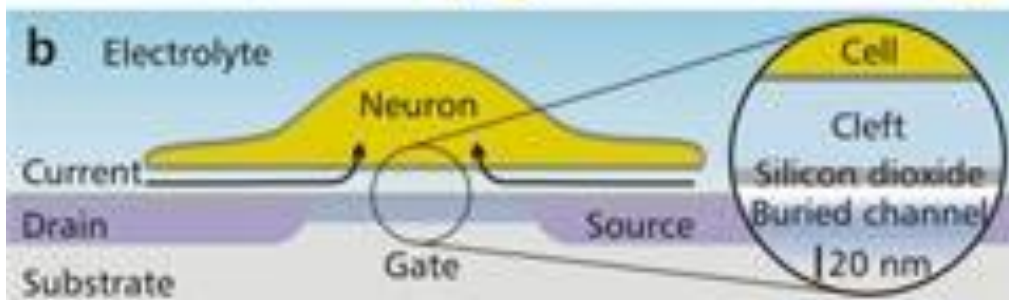
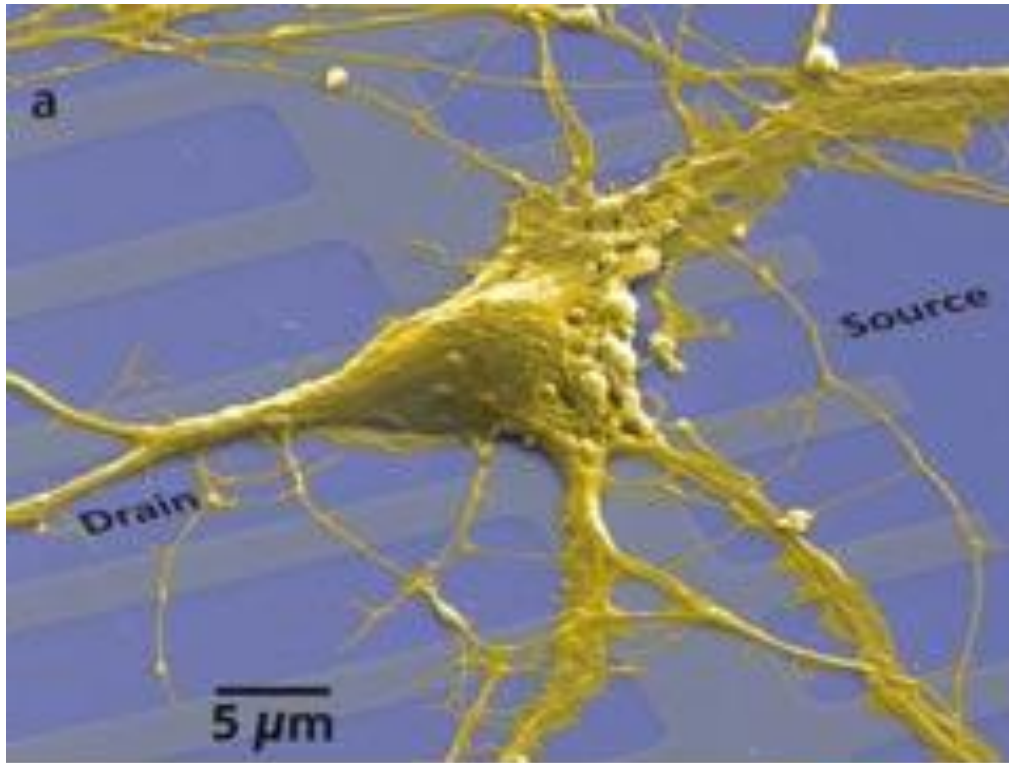
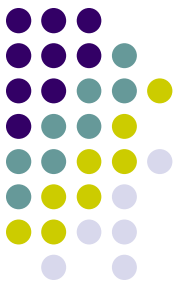
Merging silicon with LNN

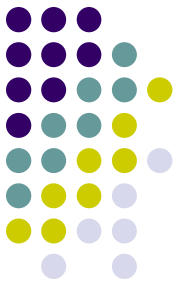
Neuromorphic MEMS

Hardware with wetware properties



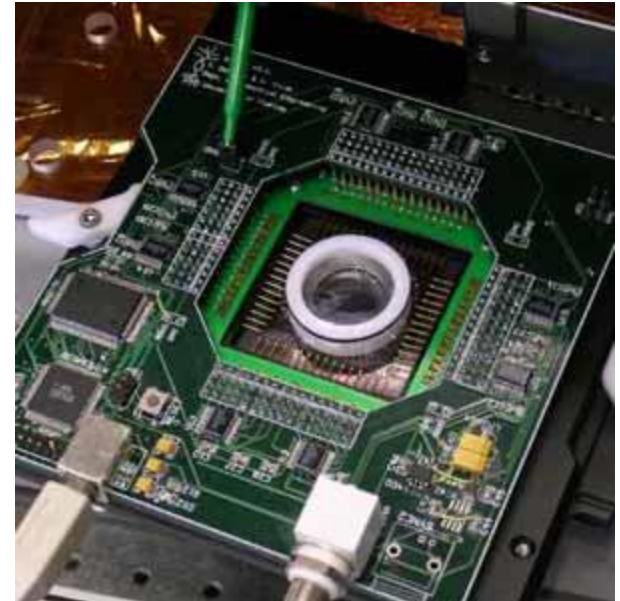
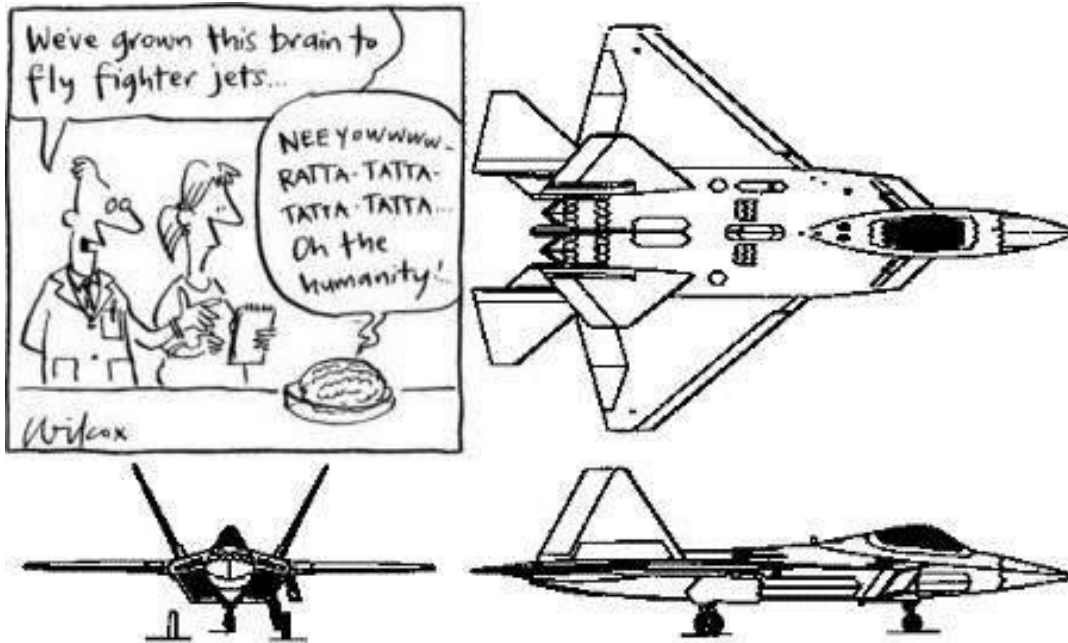
# Direct silicon neuron interconnect



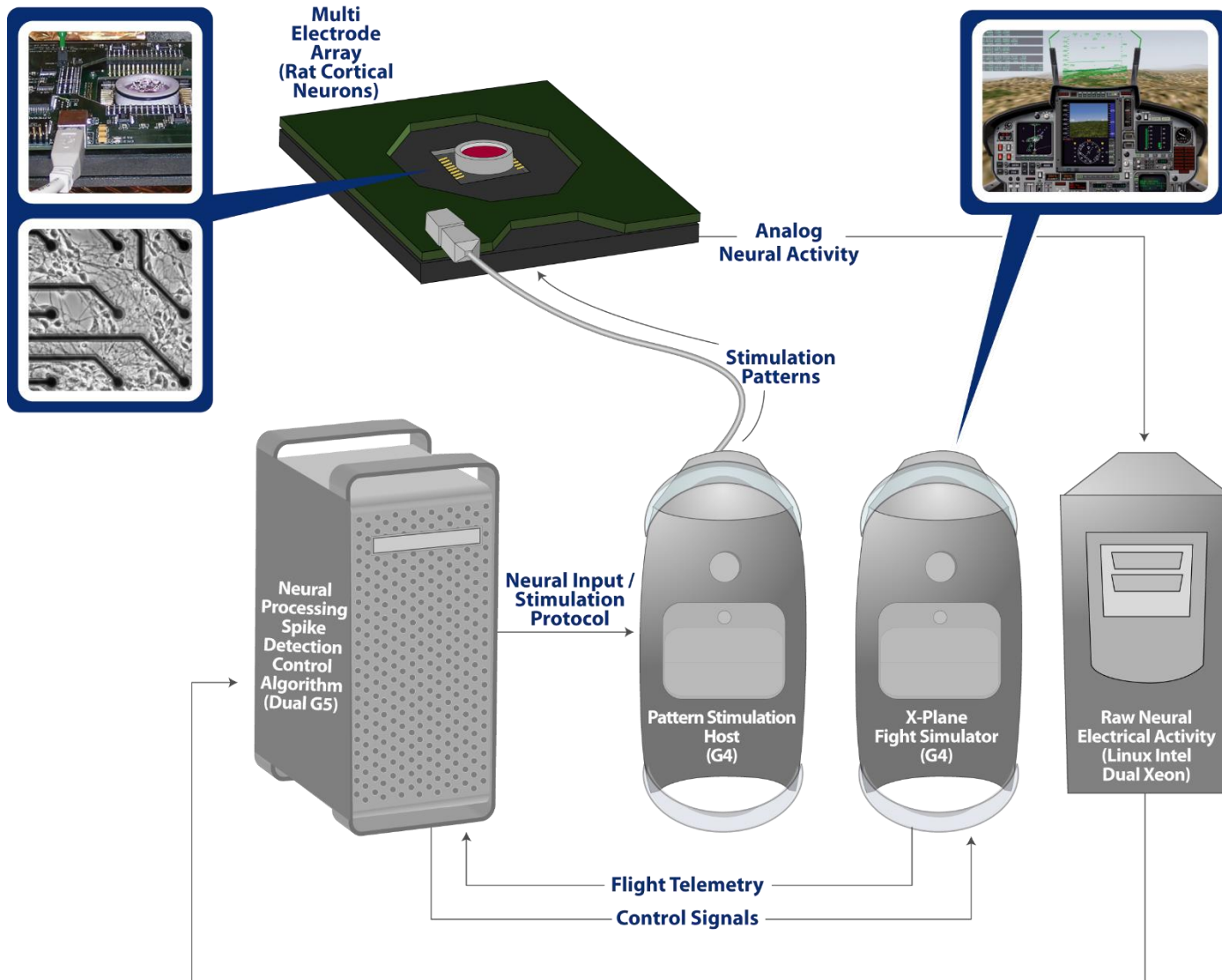
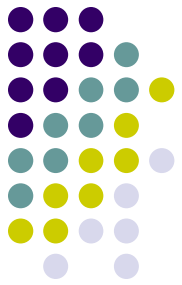


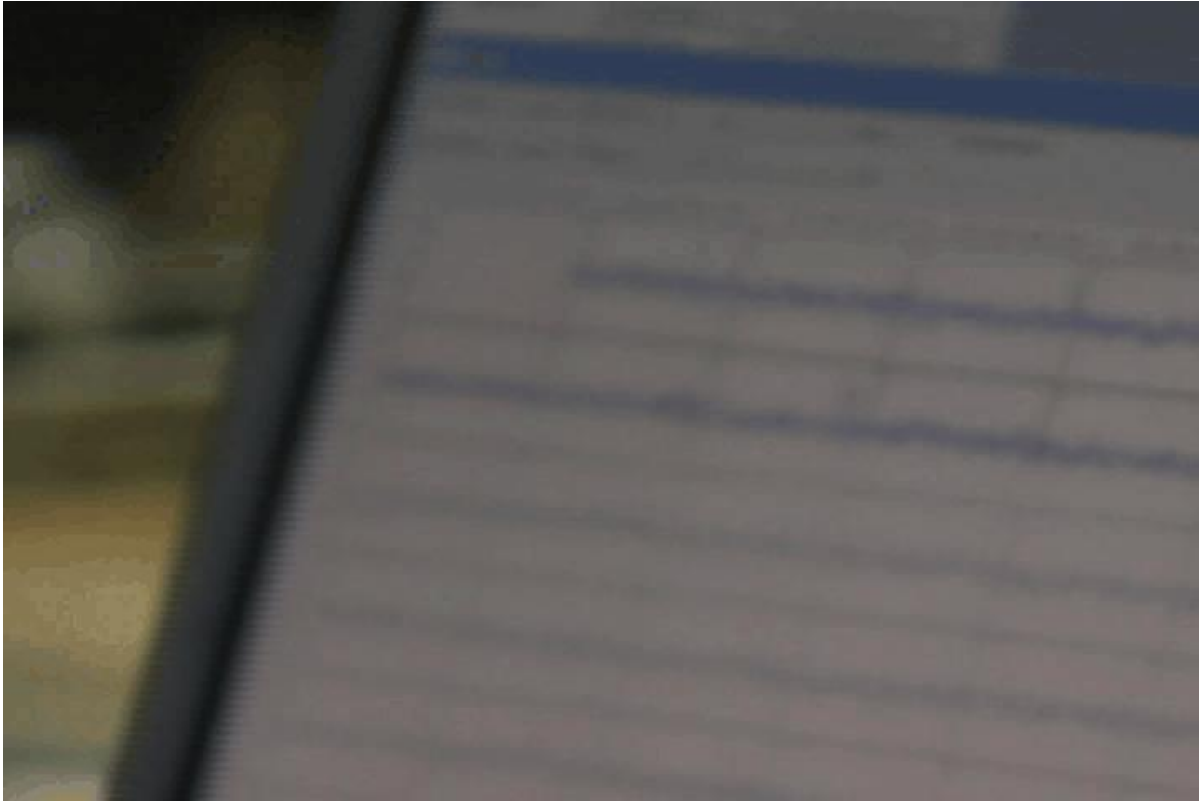
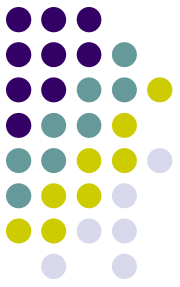
# Examples

- Flight simulator

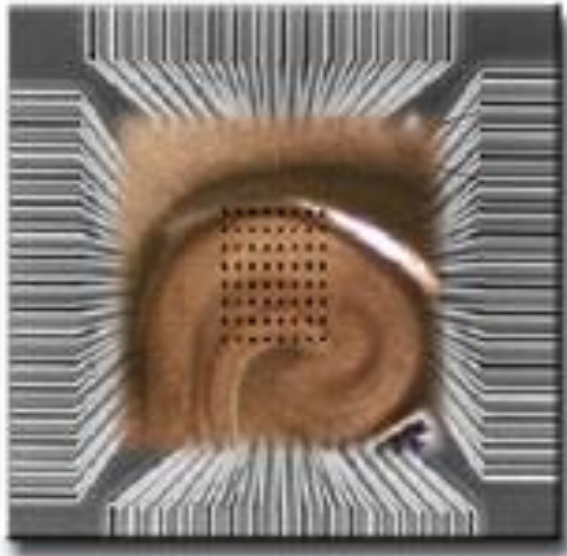




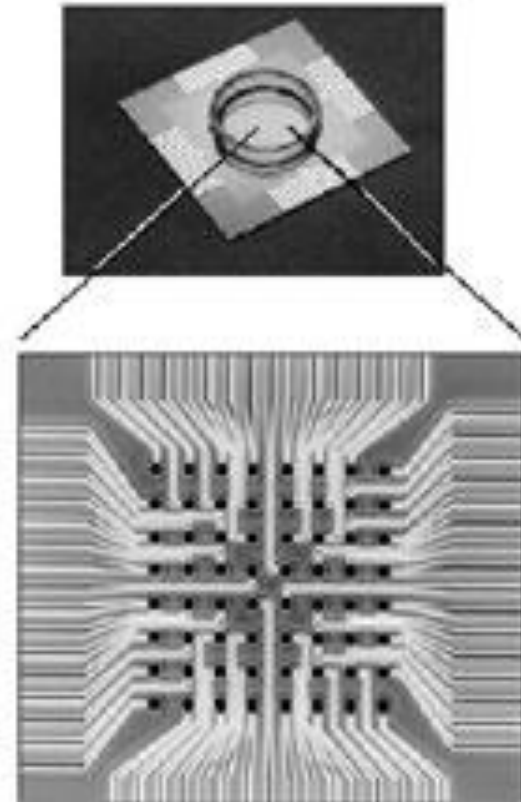


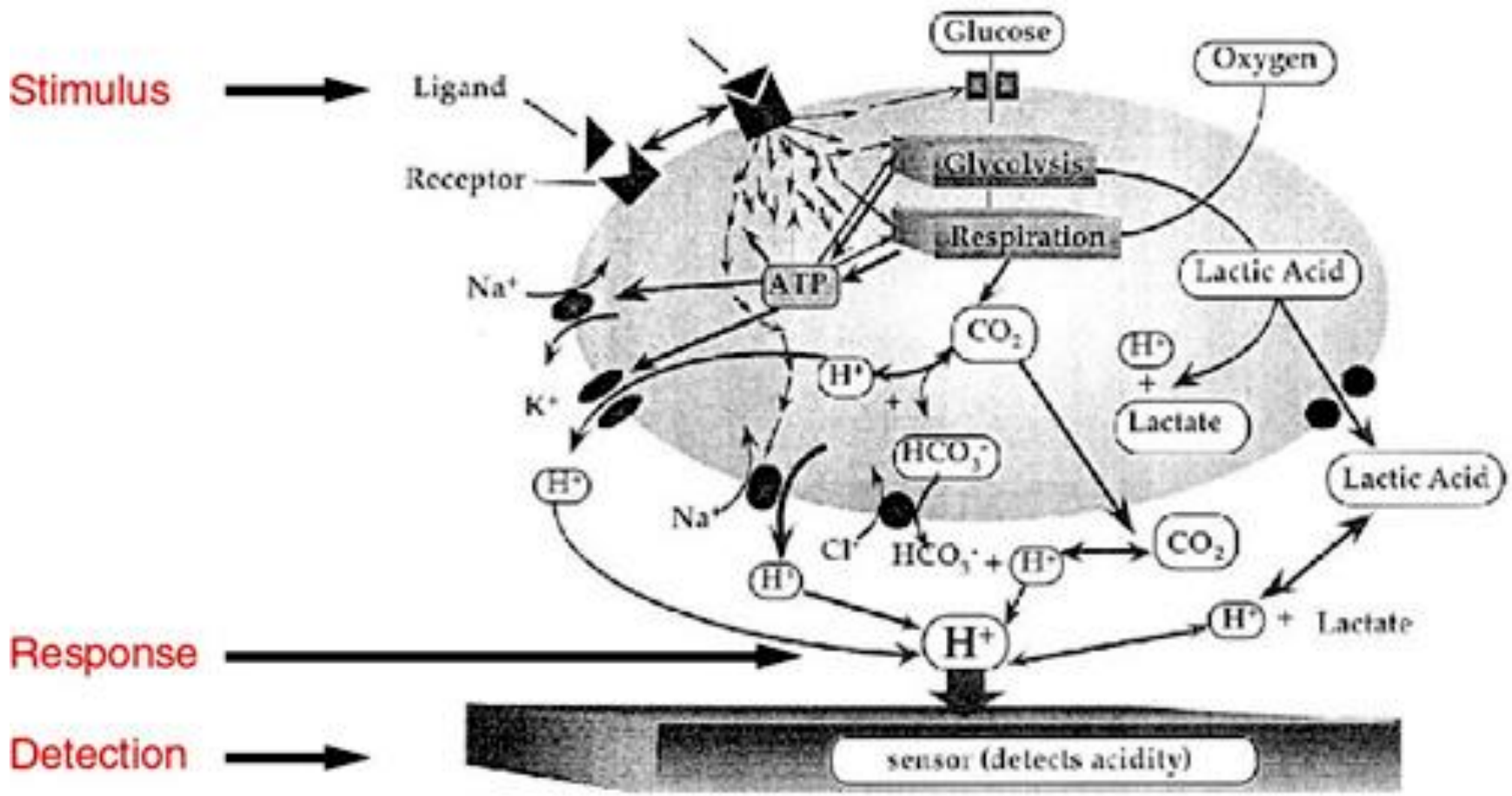
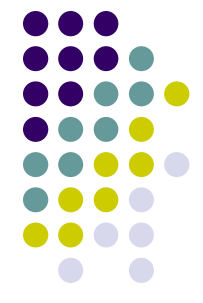


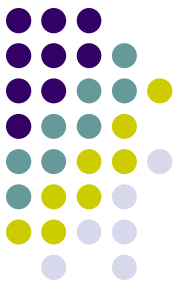
# Drug discovery



Brain-on-a-Chip™







Is it possible to study the complex signal processing of a cell with greater than 100,000 inputs that are dimensionally less than 1 micron?

Is it possible to understand the role of complex branching in signal processing?

If understood would it possible to influence branching and synapse formation?

Would it be possible to repair or rewire the circuitry with such knowledge?

# Future chips for synaptic studies

- High density of nano-electrodes ( $< 0.1 \mu\text{m}$ )
- Patterned for designer LNN

